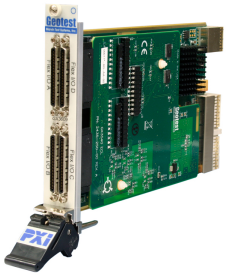
GX3609

FPGA PXI CARD WITH 80 TTL DIFFERENTIAL CHANNELS MODULE

- PXI FPGA card with 80 channel, differential TTL buffers
- Read / Write control via a software front panel or API
- Direction control configurable on a per pin basis



DESCRIPTION

The GX3609 is a 3U PXI FPGA card with 80 TTL differential channels. The GX3609 is comprised of a GX3500 FPGA card and the GX3509, 80 channel, differential TTL expansion card.

PROGRAMMING AND SOFTWARE

The board is supplied with the GXFPGA library, a software package that includes a virtual instrument panel, and a Windows 32/64-bit DLL driver library and documentation. The virtual panel can be used to interactively program and control the instrument from a window that displays the instrument's current settings and status. In addition, interface files are provided to support access to programming tools and languages such as ATEasy, LabView, LabView/Real-Time, C/C++, Microsoft Visual Basic®, Delphi, and Pascal.

An On-Line help file and PDF User's Guide provides documentation that includes instructions for installing, using and programming the board.

A separate software package - GtLinux - provides support for Linux 32/64 operating systems.

APPLICATIONS

- Automatic Test Equipment (ATE) and Functional Test
- Data Acquisition
- Process Control
- Factory Automation

SPECIFICATIONS

GX3500 FPGA CARD WITH GX3509 DIFFERENTIAL TTL EXPANSION BOARD	
Number of Channels	80 I/O signals. Direction is configurable by software on a per pin basis
Logic Family	Differential TTL, RS-485 compatible
Data Rate	35 Mb/s (max)
Driver Output Current	±60 mA (sink or source) (max)
Differential Output Voltage	1.5 V (min); 6 V (max) (no load)
Receiver Input Current	+20 μA / -100 μA
Differential Input Voltage	±12 V (max)
Power On State	All channels are configured as inputs

Note: Specifications are subject to change without notice

ORDERING INFORMATION

GX3609	3U PXI Card, Differential TTL, 80 Differential Channels (GX3500 & GX3509)

FPGA

GX3609

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