

GX3610

FPGA PXI CARD WITH 80 CHANNEL MLVDS BUFFER MODULE

- FPGA PXI card with 80 mLVDS channels
- 200 Mb/s maximum data rate
- Read / Write control via a software front panel or API
- Direction configurable on a per pin basis



DESCRIPTION

The GX3610 is a 3U PXI FPGA card with 80, mLVDS channels. The GX3610 is comprised of the GX3500 PXI FPGA card and the GX3510 expansion card providing 80 mLVDS buffered channels.

PROGRAMMING AND SOFTWARE

The board is supplied with the GXFPGA library, a software package that includes a virtual instrument panel, and a Windows 32/64-bit DLL driver library and documentation. The virtual panel can be used to interactively program and control the instrument from a window that displays the instrument's current settings and status. In addition, interface files are provided to support access to programming tools and languages such as ATEasy, LabView, LabView/Real-Time, C/C++, Microsoft Visual Basic®, Delphi, and Pascal. An On-Line help file and PDF User's Guide provides documentation that includes instructions for installing, using and programming the board.

A separate software package - [GtLinux](#) - provides support for Linux 32/64 operating systems.

APPLICATIONS

- Automatic Test Equipment (ATE) and Functional Test
- Data Acquisition
- Process Control
- Factory Automation

SPECIFICATIONS

GX3500 FPGA CARD WITH GX3510 MLVDS EXPANSION BOARD	
Number of Channels	80 I/O signals. Direction is configurable by software on a per pin basis
Logic Family	M - LVDS. Type 2 receiver
Data Rate	200 Mbps (max)
Differential Output Voltage	480 mV (min); 650 mV (max)
Differential Input Voltage Threshold	150 mV (max), positive going input 50 mV (max), negative going input
Absolute Maximum Input Voltage Range	-1.8 V to 4 V
Power on State	All channels are configured as inputs

Note: Specifications are subject to change without notice

ORDERING INFORMATION

GX3610	3U PXI Card, mLVDS, 80 Differential Channels (GX3500 & GX3510)
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